Appl. No.: 09/838,078
Amdt. dated June 22, 2004
Reply to Office action of April 16, 2004

Amendments to the Specification:

[0003] U.S. Patent Application No. 09/584,034—————————, filed May 30, 2000——————————, and entitled "Slack Fetch to Improve Performance of a Simultaneous and Redundantly Threaded Processor."~~(1662-23801)~~.

Please replace paragraph [0004] with the following amended paragraph:

[0004] U.S. Patent Application No. 09/837,995—————————, filed April 19, 2001——————————, and entitled "Simultaneously and Redundantly Threaded Processor Store Instruction Comparator."~~(1662-36900)~~.

Please replace paragraph [0005] with the following amended paragraph:

[0005] U.S. Patent Application No. 09/839,459—————————, filed April 19, 2001——————————, and entitled "Cycle Count Replication in a Simultaneous and Redundantly Threaded Processor."~~(1662-37000)~~.

Please replace paragraph [0006] with the following amended paragraph:

[0006] U.S. Patent Application No. 09/839,621—————————, filed April 19, 2001——————————, and entitled "Active Load Address Buffer."~~(1662-37100)~~.

Please replace paragraph [0007] with the following amended paragraph:

[0007] U.S. Patent Application No. 09/838,069—————————, filed April 19, 2001——————————, and entitled "Input Replicator for Interrupts in a Simultaneous and Redundantly Threaded Processor."~~(1662-37300)~~.

Please replace paragraph [0008] with the following amended paragraph:

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[0008]   U.S. Patent Application No. 09/839,626—————, filed April 19, 2001—————, and entitled "Simultaneously and Redundantly Threaded Processor Uncached Load Address Comparator and Data Value Replication Circuit." (1662-37400).

Please replace paragraph [0009] with the following amended paragraph:

[0009]   U.S. Patent Application No. 09/839,624—————, filed April 19, 2001—————, and entitled "Load Value Queue Input Replication in a Simultaneous and Redundantly Threaded Processor." (1662-37500).